

ADM-PCIE-KU3 SDAccel Platform Release Notes

Document Revision: 1.0 Sep 26, 2018



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1 Platform Release History

Note:

The SDAccel^{TW} platforms listed below will not work with SDAccel^{TW} releases earlier than the target release they were built for. Alpha Data cannot guarantee that the SDAccel^{TW} releases later than the target release SDAccel^{TW} releases later than the target release they were built for.

1.1 xilinx_adm-pcie-ku3_2ddr-xpr_4_0

1.1.1 Release 2017_4-1.0.0

Initial release of the re-packaged xilinx_adm-pcie-ku3_2ddr-xpr_4_0 platform.

This platform, originally packaged for SDAccel[™] 2017.2, was re-structured to be compatible with the new platform structure seen in SDAccel[™] 2017.4 and greater.

Date	Platform Package Release	
Sep 26, 2018	xilinx_adm-pcie-ku3_2ddr-xpr_4_0-2017_4-1.0.0.zip	

Table 1 : Table of Releases



2 Document Revision History

Date	Revision	Nature of change
Sep 26, 2018	1.0	Initial version.

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