



**ALPHA DATA**

**ADM-PCIE-7V3 SDAccel Platform  
Release Notes**

**Document Revision: 1.0  
Sep 26, 2018**

**© 2018 Copyright Alpha Data Parallel Systems Ltd.  
All rights reserved.**

**This publication is protected by Copyright Law, with all rights reserved. No part of this publication may be reproduced, in any shape or form, without prior written consent from Alpha Data Parallel Systems Ltd.**

**Head Office**

Address: 4 West Silvermills Lane  
Edinburgh, EH3 5BD, UK  
Telephone: +44 131 558 2600  
Fax: +44 131 558 2700  
email: [sales@alpha-data.com](mailto:sales@alpha-data.com)  
website: <http://www.alpha-data.com>

**US Office**

611 Corporate Circle, Suite H  
Golden, CO 80401  
(303) 954 8768  
(866) 820 9956 - toll free  
[sales@alpha-data.com](mailto:sales@alpha-data.com)  
<http://www.alpha-data.com>

**All trademarks are the property of their respective owners.**

# 1 Platform Release History

**Note:**

The SDAccel™ platforms listed below will not work with SDAccel™ releases earlier than the target release they were built for. Alpha Data cannot guarantee that the SDAccel™ platforms listed below will work with SDAccel™ releases later than the target release they were built for.

## 1.1 xilinx\_adm-pcie-7v3\_1ddr\_3\_0

### 1.1.1 Release 2017\_4-1.0.0

Initial release of the re-packaged xilinx\_adm-pcie-7v3\_1ddr\_3\_0 platform.

This platform, originally packaged for SDAccel™ 2017.2, was re-structured to be compatible with the new platform structure seen in SDAccel™ 2017.4 and greater.

Date	Platform Package Release
Sep 26, 2018	xilinx_adm-pcie-7v3_1ddr_3_0-2017_4-1.0.0.zip

**Table 1 : Table of Releases**

## 2 Document Revision History

Date	Revision	Nature of change
Sep 26, 2018	1.0	Initial version.