



## ADB3 Driver Component 1.4.21 for VxWorks Release Note

### Introduction

This release note accompanies the ADB3 Driver Component for VxWorks. The latest version of this driver can be found at:

<ftp://ftp.alpha-data.com/pub/admxrcg3/vxworks>

For support, send e-mail to [support@alpha-data.com](mailto:support@alpha-data.com)

The ADB3 Driver Component for VxWorks is a component that can be built into a VxWorks kernel image.

### Operating systems supported

This release of the ADB3 Driver Component for VxWorks supports the following operating systems:

- VxWorks 6.8 and 6.9

### Hardware supported

This release of the ADB3 Driver Component for VxWorks supports the following Alpha Data hardware:

- ADM-XRC-6TL
- ADM-XRC-6T1
- ADM-XRC-6T-DA1
- ADM-XRC-6TGE and ADM-XRC-6TGEL
- ADM-XRC-6T-ADV8
- ADPE-XRC-6T and ADPE-XRC-6T-L
- ADPE-XRC-6T-ADV
- ADM-XRC-7K1
- ADM-XRC-7V1
- ADM-VPX3-7V2
- ADM-PCIE-7V3
- ADM-PCIE-KU3
- ADM-XRC-KU1 and ADM-XRC-KU1A-T8
- ADM-PCIE-8V3
- ADM-PCIE-8K5

### License agreement

Please refer to the files **license.rtf** or **license.txt** within this software package for the licensing terms that apply to this software. Please contact Alpha Data if alternative licensing terms are required.

Alpha Data reserves the right to use different licensing terms for future releases of this software.

## Installation

### Installation in a Windows VxWorks host

To install the ADB3 Driver Component, follow these steps:

- Unzip the **vxbaDb3-1.4.21.zip** into the **%WIND\_BASE%** folder. If prompted about whether or not to allow merging of folders, respond with "allow".
- Next, the ADB3 Driver Component must be compiled for the CPU architecture of your kernel image. To do this, start a "VxWorks Development Shell" and enter the following commands:

```
cd /d %WIND_BASE%\3rdparty\alphadata
make CPU=<cpu> TOOL=<tool> VXBUILD="<options>"
```

In the **make** command, **<cpu>**, **<tool>** and **<options>** must match the way your kernel image is built. For example, with most x86\_64 SMP kernels, the **make** command is:

```
make CPU=NEHALEM TOOL=gnu VXBUILD="LP64 SMP"
```

For 32-bit, non-SMP kernel images, **VXBUILD** is generally omitted; for example:

```
make CPU=PPC604 TOOL=diab
```

#### Note

The effect of building the ADB3 Driver Component for a particular CPU architecture is to create (or update) a vendor-specific archive with the object modules generated during compilation. For reference, the full pathname of this library is:

```
%WIND_BASE%\target\lib<_options>\<arch>\<cpu>\common\libalphadata.a
```

The individual object files that were stored in the archive are placed in:

```
%WIND_BASE%\target\lib<_options>\<arch>\<cpu>\common\alphadata
```

If the ADB3 Driver Component is successfully compiled for the CPU architecture used by your VxWorks kernel image, you will see it in the VxWorks kernel image configurator and will be able to include it. Otherwise, you will typically see that it is present in the VxWorks kernel image configurator but grayed out and cannot be included.

### Installation in a Linux VxWorks host

To install the driver in Linux, follow these steps:

- Extract the **vxbaDb3-1.4.21.tar.gz** into the **\$WIND\_BASE** directory.
- Next, the ADB3 Driver Component must be compiled for the CPU architecture of your kernel image. To do this, start a "VxWorks Development Shell" and enter the following commands:

```
cd $WIND_BASE/3rdparty/alphadata
make CPU=<cpu> TOOL=<tool> VXBUILD="<options>"
```

In the **make** command, **<cpu>**, **<tool>** and **<options>** must match the way your kernel image is built. For example, with most x86\_64 SMP kernels, the **make** command is:

```
make CPU=NEHALEM TOOL=gnu VXBUILD="LP64 SMP"
```

For 32-bit, non-SMP kernel images, **VXBUILD** is generally omitted; for example:

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```
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```

The individual object files that were stored in the archive are placed in:

```
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```

If the ADB3 Driver Component is successfully compiled for the CPU architecture used by your VxWorks kernel image, you will see it in the VxWorks kernel image configurator and will be able to include it. Otherwise, you will typically see that it is present in the VxWorks kernel image configurator but grayed out and cannot be included.

## Uninstallation

### Uninstallation in a Windows VxWorks host

To uninstall the driver, first ensure that you have removed the component **DRV\_ALPHADATA\_VXBADB3** from all of your kernel image projects. Then:

- Manually delete the following folder and its contents:

```
%WIND_BASE%\target\3rdparty\alphadata\vxbaadb3
```

- Manually delete the following files:

```
%WIND_BASE%\target\config\comps\src\hwif\vxbaadb3.dc  
%WIND_BASE%\target\config\comps\src\hwif\vxbaadb3.dr  
%WIND_BASE%\target\config\comps\vxworks\40vxbaadb3.cdf
```

You may also wish to remove any object modules and archives built from Alpha Data source code, as a result of following the instructions in [Installation in a Windows VxWorks host](#). This can safely be done without damaging your VxWorks installation if only the following folder is deleted:

- ```
%WIND_BASE%\target\lib<_options>\<arch>\<cpu>\common\alphadata
```

and only the following file is deleted:

- ```
%WIND_BASE%\target\lib<_options>\<arch>\<cpu>\common\libalphadata.a
```

#### Note

In the VxWorks component build system, source code for all components from a particular vendor is built into the vendor-specific archive:

```
%WIND_BASE%\target\lib<_options>\<arch>\<cpu>\common\lib<vendor>.a
```

It is not possible to selectively delete compiled object modules from the vendor-specific archive. Thus, deleting the vendor-specific archive will delete all Alpha Data object code, not just that of the ADB3 Driver. If you have any other Alpha Data components installed, you must rebuild them afterwards.

### Uninstallation in a Linux VxWorks host

To uninstall the driver, first ensure that you have removed the component **DRV\_ALPHADATA\_VXBADB3** from all of your kernel image projects. Then:

- Manually delete the following directory and its contents:

```
$WIND_BASE/target/3rdparty/alphadata/vxbAdb3
```

- Manually delete the following files:

```
$WIND_BASE/target/config/comps/src/hwif/vxbAdb3.dc  
$WIND_BASE/target/config/comps/src/hwif/vxbAdb3.dr  
$WIND_BASE/target/config/comps/vxWorks/40vxbAdb3.cdf
```

You may also wish to remove any object modules and archives built from Alpha Data source code, as a result of following the instructions in [Installation in a Linux VxWorks host](#). This can safely be done without damaging your VxWorks installation if only the following folder is deleted:

- `$WIND_BASE/target/lib<_options>/<arch>/<cpu>/common/alphadata`

and only the following file is deleted:

- `$WIND_BASE/target/lib<_options>/<arch>/<cpu>/common/libalphadata.a`

#### Note

In the VxWorks component build system, source code for all components from a particular vendor is built into the vendor-specific archive:

```
$WIND_BASE/target/lib<_options>/<arch>/<cpu>/common/lib<vendor>.a
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## VPD write-protection mechanism

The VPD write-protection mechanism described in the ADM-XRC Gen 3 SDK User Guide is exposed via the VxWorks kernel image configurator. If `VXBADB3_ENABLE_VPD_WRITE` is **TRUE**, the ADB3 Driver permits writes to VPD memory.

Note that for certain models, a board may additionally need to be operating in "Service Mode" in order to permit writes to VPD memory. Please refer to the User Manual for your reconfigurable computing board to determine whether or not "Service Mode" applies, and how to enter it.

`VXBADB3_ENABLE_VPD_WRITE` is the initial value of the global integer variable `adb3DrvEnableVpdWrite`. If necessary, this variable can be manipulated at runtime in order to enable or disable writes to VPD memory. This variable is checked every time an application attempts to write to the VPD memory, so changes to this variable take effect immediately rather than being sampled when the driver is started.

To set this value to 1 (thus enabling VPD writes at driver level) using the VxWorks shell, use:

```
-> adb3DrvEnableVpdWrite=(int)1
```

To set this value to 0 (thus disabling VPD writes at driver level) using the VxWorks shell, use:

```
-> adb3DrvEnableVpdWrite=(int)0
```

## Common buffer support

The ADB3 Driver can allocate one or more "common buffers" at startup. The purpose of this feature is to support applications that use Direct Master data transfer, such as an ethernet-style I/O interface where the sizes and arrival times of packets of data are not known in advance by software running on the host. These buffers have the following characteristics:

- Persist until the driver is stopped.

- Guaranteed aligned to a specified power-of-2 address boundary.
- Allocated from the appropriate pool of memory in order to be contiguous and visible to bus-master devices.
- Can be mapped into the virtual address space of a user-mode process; see "ADMXRC3 API Specification 1.5.0" or later for details of the new ADMXRC3 API functions relating to common buffers.

This feature is exposed via the VxWorks kernel image configurator:

- **VXBADB3\_NUM\_COMMON\_BUFFER** determines the number of common buffers allocated; the default is zero, meaning that no common buffers are allocated by default.
- **VXBADB3\_COMMON\_BUFFER\_SIZE** determines the size in bytes of each common buffer; the default is 64 KiB (0x10000).
- **VXBADB3\_COMMON\_BUFFER\_ALIGN** determines the power-of-2 address boundary to which each common buffer is guaranteed to be aligned; the default is 32 bytes.

## Known issues

### Modifications to certain BSPs needed for interrupt delivery

Certain BSPs for single-board computers require a modification to an interrupt vector table in order for interrupts to be delivered to the ADB3 Driver. If the driver behaves as if interrupts are not being delivered to it - for example, if DMA transfers hang, or the "ITest" example from the ADM-XRC Gen 3 SDK fails to work correctly - it may be necessary to modify the file **hwConf.c** in your VxWorks kernel image project.

Although Alpha Data cannot in general provide precise instructions for doing this, for many BSPs the necessary steps are as follows:

- 1 Open **hwConf.c** in your VxWorks kernel image project in your favorite editor, and locate a table of this form:

```
LOCAL const struct intrCtrlInputs IoApicInputs[] = {
    { VXB_INTR_DYNAMIC, "yn", 0, 0 },
    { VXB_INTR_DYNAMIC, "gei", 0, 0 },
    ... other entries ...
#ifdef INCLUDE_HPET_MSI
    { INT_NUM_IA_HPET_TIMER0, "iaHpetTimerDev", 0, 0 },
    ... other entries ...
#endif /* INCLUDE_HPET_MSI */
    ... other entries ...
};
```

- 2 If you have a single Alpha Data Gen 3 Reconfigurable Computing Device in the target system, add the following entry to the end of the table:

```
{ VXB_INTR_DYNAMIC, "adb3", 0, 0 }
```

If you have more than one device, add as many entries as you have devices, incrementing the number in the 3rd position of each entry for each entry. For example, if there are 4 devices, add the following entries to the end of the table:

```
{ VXB_INTR_DYNAMIC, "adb3", 0, 0 },
{ VXB_INTR_DYNAMIC, "adb3", 1, 0 },
{ VXB_INTR_DYNAMIC, "adb3", 2, 0 },
{ VXB_INTR_DYNAMIC, "adb3", 3, 0 }
```

- 3 Rebuild your VxWorks kernel image. It should now be capable of delivering interrupts to the ADB3 Driver.

## Fixed-local addressing DMA transfers

The flag `ADMXRC3_DMA_FIXEDLOCAL` when used with the DMA functions in the `ADMXRC3` currently has no effect for Gen 3 hardware.

## Compiler warnings during builds

The following compiler warnings may be generated when building the driver for certain configurations:

- `../././modules/admxrc3/admxrc3.c(858) (col. 45): warning #13365: XMM register was modified, but CG_allow_xmm was not specified`  
This warning arises because the 64-bit build specs for the Intel Compiler for VxWorks set the `-fno-implicit-fp-sse` option (no implicit floating point or SSE). This warning should appear only when building the `ADMXRC3` API library, and can be safely ignored.
- `.././framework/vxworks/vxbus_pci.c: In function 'getRawBusResources':`  
`.././framework/vxworks/vxbus_pci.c:137: warning: unused variable 'f'`  
This warning arises from variables that are required in some VxWorks configurations and not in others, and can safely be ignored.
- `icc: command line warning #10006: ignoring unknown option '-Wbad-function-cast'`  
`icc: command line warning #10006: ignoring unknown option '-Wno-sign-conversion'`  
This warning arises because the 64-bit build specs for the Intel Compiler for VxWorks set a couple of compiler options that are not supported for that particular version of the compiler. These warnings can safely be ignored.
- `".././framework/vxworks/legacy_methods.c", line 154: warning (dcc:1500): function pciIntDisconnect2 has no prototype`  
This warning can be ignored, and occurs because the `pciIntDisconnect2` function appears to be missing from the VxWorks 5.5 header files, although it exists and can be used in some VxWorks kernel images.

## Release history

### Release 1.4.21

This release implements `ADMXRC3` API Specification version 1.8.3.

Enhancements:

- Added support for `ADM-XRC-KU1A-T8`.
- The ADB3 Driver Utilities are now provided as an optional component that may be included in a VxWorks Image Project (VIP), to remove the need to create a separate Workbench project for them.

### Release 1.4.19

This release implements `ADMXRC3` API Specification version 1.8.3.

Enhancements:

- Added a function `ADB3_CommandAVR` to `ADB3` API. This command permits direct communication with the microcontroller on the following models: `ADPE-XRC-6T(-L)`, `ADPE-XRC-6T-ADV`, `ADM-XRC-7K1`, `ADM-XRC-7V1`, `ADM-VPX3-7V2`.

Corrections:

- Fixed a regression bug introduced in the 1.4.18 release, which prevented a DMA transfer from being aborted by any means (killing the thread that initiated it or calling `ADMXRC3_CancelDMA`).

### Release 1.4.18

This release implements `ADMXRC3` API Specification version 1.8.3.

Enhancements:

- 1 For Bridge-in-Target FPGA designs, which are exposed as a device whose model code is `ADMXRC3_MODEL_GENERIC`, FPGA interrupts can now be consumed by user-mode applications, via API functions such as `ADMXRC3_RegisterWin32Event` and `ADMXRC3_StartNotificationWait`.

Corrections:

- 2 Corrected maximum number of DMA engines exposed for ADM-XRC-KU1-P5HI IP; was 2 (incorrect), now 4 (corrected).
- 3 Corrected incorrect package returned by `ADMXRC_GetFpgaInfo(A,W)` for ADM-XRC-KU1 fitted with KU060 FPGA; was FLVA1517 (incorrect), now FFVA1517 (corrected).
- 4 For ADM-XRC-7Z1 only, corrected failure to restore FPGA interrupt enable on power-up; symptom is that after powering up from a low-power state, FPGA interrupts no longer work.
- 5 Corrected a bug affecting the ADM-XRC-KU1, ADM-PCIE-8V3 & ADM-PCIE-8K5 where `ADMXRC3_WriteVPD` with a block of data whose VPD space address does not start and end on a 512-byte boundary causes the data in the affected 512-byte page of VPD space memory to be shifted down by 2 bytes, cumulative with each such write.
- 6 Corrected sensor names and units for the ADM-PCIE-8K5, as reported by `ADMXRC3_GetSensorInfo`, which had incorrectly been copied and pasted from definitions for the ADM-PCIE-8V3.
- 7 For the ADM-PCIE-8K5, sensor 15 now correctly reflects the FPGA temperature. Previously, it erroneously reflected the board temperature. A new sensor, with index 18, now reflects the board temperature.

New behavior:

- 8 Device nodes in `/dev` are now named `/dev/adb3i*` & `/dev/admxrc3i*` so that the number suffix does not appear to begin with the digit 3.
- 9 `ADMXRC3_DMA` API calls now accept a new flag, `ADMXRC3_DMA_BYPASSHW`. This is used by Alpha Data for testing the driver.

## Release 1.4.17

This release implements ADMXRC3 API Specification version 1.8.2.

Enhancements:

- 1 Added support for the ADM-XRC-KU1, ADM-PCIE-8V3 and ADM-PCIE-8K5.
- 2 Added support for the secondary PCI Express Gen3 x8 endpoint of Alpha Data's ADM-PCIE-KU3-HI2 IP. This requires a system in which the x16 PCIe slot containing the ADM-PCIE-KU3 resides is bifurcated into two x8 links.
- 3 Added DWORD driver parameters **`Adpexrc6tForcePcieGen`** & **`Adpexrc6tlForcePcieGen`**, to work around around the issue of PCIe Gen2 link speed not being automatically negotiated on a particular motherboard (Congatec TS77-i3-3217UE). Set these parameters to 1 or 2 to force retraining to PCIe Gen 1 or 2 (respectively) link speed when the driver starts. Default of 0 means no forced retraining is performed.

Currently, this workaround is performed only for the ADPE-XRC-6T (controlled by driver parameter **`Adpexrc6tForcePcieGen`**) or for the ADPE-XRC-6T-L (controlled by driver parameter **`Adpexrc6tlForcePcieGen`**).

## Release 1.4.16

This release implements ADMXRC3 API Specification version 1.7.3.

Enhancements:

- 1 Added clock programming support for the ADM-PCIE-7V3. Six reference clocks are now exposed via the ADMXRC3 API.
- 2 Added support for ADM-PCIE-7V3 sensors. Requires that the PCI revision of the ADM-PCIE-7V3 ADB3 IP is 0x03 or later for the driver to expose the sensors via the ADMXRC3 API.

- 3 Added support for the ADM-PCIE-KU3.
- 4 Added soft-reconfiguration functions using IPROG to ADB3 API, for Bridgeless models (initially ADM-PCIE-7V3 & ADM-PCIE-KU3): **ADB3\_AbortIPROG**, **ADB3\_ScheduleIPROG**, **ADB3\_StatusIPROG**
- 5 On ADM-PCIE-7V3, if PCIe revision is 0x06 or higher, enables 8 simultaneous packets in flight for increased DMA performance.

Corrections:

- 6 Fixed a regression bug in ADB3 Driver 1.4.14 & 1.4.15 that affects (only) the ADPE-XRC-6T, ADPE-XRC-6T-L & ADPE-XRC-6T-ADV. In the aforementioned two driver releases, if the system monitor generates its "alert" interrupt due to an out-of-range condition on any monitored power supply or temperature sensor, high utilization (40% - 90%) of one CPU core occurs for as long as the out-of-range condition persists, due to failure by the ADB3 Driver to properly service the alert interrupt.

## Release 1.4.15

This release implements ADMXRC3 API Specification version 1.7.1.

Enhancements:

- 1 Added support for the ADM-PCIE-7V3. NOTE: Sensor functionality for the ADM-PCIE-7V3 is not included in this version of the driver, but will be added in a future release; the number of sensors currently exposed by the ADMXRC3 API is 0.
- 2 Changed the default value of the driver parameter `VXBADB3_COMMON_BUFFER_ALIGN` to 32 (0x20), to accommodate the ADM-PCIE-7V3, whose native AXI4 / OCP internal bus width is 32 bytes.

## Release 1.4.14

This release implements ADMXRC3 API Specification version 1.7.1.

This is the first release of the ADB3 Driver Component for VxWorks, and is broadly feature-equivalent to ADB3 Driver for VxWorks 1.4.14.