



ADB3 Driver 1.4.3 for Linux Release Note

Introduction

This release note accompanies the ADB3 Driver for Linux. The latest version of this driver can be found at:

<ftp://ftp.alpha-data.com/pub/admxrcg3/linux>

For support, send e-mail to support@alpha-data.com

Operating systems supported

This release of the ADB3 Driver supports the following operating systems:

- GNU/Linux distribution with 2.6.x kernel

Due to the ever-changing nature of GNU/Linux, Alpha Data cannot guarantee that this driver can be successfully configured, built, installed and run on all Linux distributions past, present and future. Alpha Data makes best-efforts to ensure compatibility with all Linux distributions, but should a problem be encountered, please contact support@alpha-data.com.

Hardware supported

This release of the ADB3 Driver supports the following Alpha Data hardware:

- ADM-XRC-6TL
- ADM-XRC-6T1
- ADM-XRC-6TGE
- ADM-XRC-6T-ADV8

This release of the ADB3 Driver has preliminary (non-finalized) support for the following Alpha Data hardware:

- ADPe-XRC-6T and ADPe-XRC-6T-L

License Agreement

This release of software is licensed according to the terms of GNU Public License Version 2 (GPL V2). A copy of this license can be found in the file **gpl-2.0.txt** within this software package. Please contact Alpha Data if alternative licensing conditions are required.

Alpha Data reserves the right to use a different license agreement for future releases of this software.

Installation instructions

This release of the driver is distributed in source code form as a tarball (.tar.gz file extension). Please refer to the README file inside the tarball for instructions on how to configure, build and install the driver.

Completely uninstalling the driver

To uninstall the driver, first stop the driver by issuing the command **rmmod adb3**. Then delete the following files and symbolic links, if they exist:

1. `/usr/lib/libadmxcrc3.*`, `/usr/lib/libadb3.*`
2. `/usr/lib64/libadmxcrc3.*`, `/usr/lib64/libadb3.*` (if they exist)
3. `/lib/modules/<kernel version>/kernel/drivers/addon/adb3/adb3.ko`
4. `/etc/init.d/adb3`
5. `/etc/udev/rules.d/51-admxcrc3.rules`, `/etc/udev/rules.d/51-adb3.rules`

udev support

Beginning with release 1.2.1, the script `adb3.rc` provided for starting previous versions of the driver is not required, except on Linux systems that do not have the `udev` subsystem. As of release 1.2.1, when the kernel module `adb3.ko` is loaded, the `udev` subsystem automatically creates device nodes `/dev/admxcrc3*` and `/dev/adb3*`.

In order for this to work correctly, the driver must have been installed in the normal way (that is, by executing a command such as `make all install` as root). The install procedure copies the `udev` rules files `51-admxcrc3.rules` and `51-adb3.rules` to `/etc/udev/rules.d/`.

After installation, and before loading `adb3.ko`, the rules file `51-admxcrc3.rules` may be customized to change the permissions and/or owner and/or group of the device nodes `/dev/admxcrc3*`. Please see the comments in `/etc/udev/rules.d/51-admxcrc3.rules` after installing the driver for further details.

NOTE: The Makefile for the driver still installs the script `adb3.rc` in `/etc/init.d/`, but it should not be used unless there is no `udev` subsystem.

VPD write-protection mechanism

The VPD write-protection mechanism described in the ADM-XRC Gen 3 SDK User Guide is implemented as of release 1.1.0. To enable write-to-VPD, the kernel module parameter `EnableVpdWrite` must be nonzero.

This value takes effect when the driver starts, so it can be changed only by unloading the driver and restarting the driver with a different value for `EnableVpdWrite`. If this parameter is not specified, the driver considers it to be zero (write-to-VPD disabled).

Security considerations

By default, the `udev` rules file `51-admxcrc3.rules` creates device nodes in `/dev` as follows:

- Mode: 664 (octal) => owner read, owner write, group read, group write, other read
- UID: `root`
- GID: `root`

This means that the default permissions are that (i) only `root` and members of the group `root` can open devices, in read-only or read-write mode and (ii) users that are not members of the group `root` can open a device in read-only mode. However, after installing the driver, the file `/etc/udev/rules.d/51-admxcrc3.rules` can be customized to relax permissions. See the comments in that file for details.

Known issues

Downgrading to an earlier version

When downgrading to an earlier version of the driver, remove all files named `/usr/lib/libadmxcrc3.*` and `/usr/lib/libadb3.*`, (and `/usr/lib64/libadmxcrc3.*` and `/usr/lib64/libadb3.*` if on a 64-bit bi-architecture machine), before executing the `make install` command as root. Otherwise, the shared libraries remaining from the later version of the driver will be preferred by the system as they have a higher version number.

Additionally, delete any device nodes `/dev/admxrc3*` before installing the earlier driver version, as a safety precaution. This is recommended because earlier versions of the driver have different permissions mechanisms when opening devices.

Fixed-local addressing DMA transfers

The flag `ADMXRC3_DMA_FIXEDLOCAL` currently has no effect for the `ADM-XRC-6TL`, `ADM-XRC-6T1`, `ADM-XRC-6TGE` and `ADM-XRC-6T-ADV8` when used with the DMA functions in the `ADMXRC3` API.

Release history

Release 1.4.3

This release implements `ADMXRC3` API Specification version 1.4.0.

Corrections:

1. Fixed a crash that can occur when attempting to do two or more DMA transfers on the same DMA channel.
2. Fixed a crash that can occur if the driver is somehow called by a `libadmxrc3*`.so from a different driver version, due to the handlers for `ADMXRC3_GetSensorInfo` and `ADMXRC3_ReadSensor` failing to properly validate arguments.
3. Fixed an issue specific to the `ADM-XRC-6T-ADV8` where the driver emitted the debug message `**** avrInit: failed to get AVR uC firmware version*`.
4. Fixed a crash whose frequency depends on the rate of calling `poll()` or `select()` on an `ADB3` device when doing small non-blocking DMA transfers.

Release 1.4.1

This release implements `ADMXRC3` API Specification version 1.4.0.

New behavior:

1. Default permissions for `/dev/admxrc3*` device nodes are now 664 (in previous releases they were 660). This brings the `ADB3` Linux driver's behaviour into line with the `ADB3` Windows driver.

Corrections:

2. Fixed a bug in the `Si5338` clock synthesizer code for the `ADM-XRC-6TGE` that could corrupt memory when programming clock index 4. This clock generator is only available when the `Si5338ExposeAllClocks` driver parameter is nonzero; by default it is not available.
3. Support for `ADM-XRC-6T-ADV8` is now feature-complete; added support for programming VPD and reading system monitor sensors via `ADMXRC3` API.
4. Fixed a number of PowerPC-specific build issues, so that cross-building using Embedded Linux Development Kit is considerably simpler than before.

Release 1.3.1

This release implements `ADMXRC3` API Specification version 1.3.0.

New behavior:

1. Added support for the `ADM-XRC-6TGE`.
2. Added preliminary support for the `ADM-XRC-6T-ADV8`.

Enhancements:

- Now exposes (via the ADMXRC3 API) a programmable clock generator with index 0 on the ADM-XRC-6T1 when it has firmware 1.6 (PCI revision 0x06) or later.

Corrections:

- ADMXRC3_GetClockFrequency now correctly returns the current clock frequency for a given clock generator. The driver now interrogates the hardware at startup to determine the current frequencies generated by each clock generator, so that ADMXRC3_GetClockFrequency can return the correct frequency even before any call to ADMXRC3_SetClockFrequency.
- ADMXRC3_GetClockFrequency now correctly validates the pointer argument (3rd argument) passed to it, and returns ADMXRC3_NULL_POINTER if it is NULL.
- Corrected the maximum frequency allowed for the clock generator with index 0 on the ADM-XRC-6TL. Previously, the driver incorrectly permitted frequencies up to 210 MHz to be requested, whereas 140 MHz is the correct maximum frequency.
- Fixed ADMXRC3_EraseFlash failing to correctly validate the region specified to ensure that it is wholly within the unprotected region of a Flash memory bank.

Release 1.2.1

This release implements ADMXRC3 API Specification version 1.2.0.

New behavior:

- Added **udev** support. Uses the **udev** subsystem for creating device nodes in **/dev/**; the script **adb3.rc** is no longer required, except on Linux distributions that do not use **udev**.
- For ADM-XRC-6TL, now recognizes "Extended" temperature range value (2) in VPD at offset 0x3E.
- For ADM-XRC-6T1, now recognizes "Extended" temperature range value (2) in VPD at offset 0x42.

Corrections:

- Fixed a race condition that occurs in (at least) CentOS 5.6, between the default catch-all **udev** rule and the **adb3.rc** script. This race may result in unpredictable permissions for the **/dev/admxrc3*** device nodes. As of release 1.2.1, **udev** is used for creating device nodes and the **adb3.rc** script is obsoleted except for Linux distributions that do not use **udev**.

This issue affects any Linux distribution that has a catch-all **udev** rule which automatically creates device nodes in **/dev** for devices that do not match any other rule. CentOS 5.6 is among the Linux distributions that show this behaviour.
- Fixed a regression bug in the "configure" script of the 1.2.0 release in detecting the correct directory in which to install the **libadmxrc3.so** and **libadb3.so** shared libraries. In 1.2.0, if a non-biarchitecture build and install is performed on a bi-architecture machine, the 64-bit native libraries are (incorrectly) installed in **/usr/lib/** instead of (correctly) in **/usr/lib64/**.

Release 1.2.0

This release implements ADMXRC3 API Specification version 1.2.0.

New behavior:

- Bi-architecture build is now not performed by default, as most 64-bit Linux distributions do not install (by default) the necessary compatibility packages for building 32-bit binaries. To build both 32-bit and 64-bit binaries for the API libraries, specify '-biarch yes' when running the 'configure' script.

Corrections:

2. Fixed some build problems relating to kernel features in RHEL / CentOS 5.5 kernels (derived from Linux 2.6.18). The 'configure' script now detects those kernel features correctly.
3. Fixed a thumping issue that may occur if the 32-bit and 64-bit C compilers on the same bi-architecture-capable system have different struct packing rules, where certain ADMXRC3 API calls made by a 32-bit executable running under a 64-bit Linux kernel fail with ADMXRC3_UNKNOWN_ERROR.
4. Fixed a problem where, when a process closes a device handle that has ongoing non-blocking operations, the process may crash due to incorrect cleanup being performed by the driver.

Enhancements:

5. Added new API functions for performing DMA transfers with 64-bit local addresses:
 - [ADMXRC3_ReadDMAEx](#)
 - [ADMXRC3_ReadDMALockedEx](#)
 - [ADMXRC3_StartReadDMAEx](#)
 - [ADMXRC3_StartReadDMALockedEx](#)
 - [ADMXRC3_StartWriteDMAEx](#)
 - [ADMXRC3_StartWriteDMALockedEx](#)
 - [ADMXRC3_WriteDMAEx](#)
 - [ADMXRC3_WriteDMALockedEx](#)
6. Added support for new sensors in ADM-XRC-6TL and ADM-XRC-6T1 with firmware 1.4 or later. This provides additional sensors that show internal temperature and voltages in the PCI Express to OCP Bridge.
7. Changed the way that permissions are checked on opening a device to allow for a more flexible system that can be customized via the `adb3.rc` script. See the section "[Security considerations](#)" and the comments in the file `driver/adb3.rc` for details.
8. Made the driver's build system more compatible with cross-compilation, now using the same ARCH and CROSS_COMPILE environment variables as the Linux kernel. See the README file for details.

Release 1.1.2

Corrections:

1. Fixed a user-mode memory leak that can occur when the API functions [ADMXRC3_LoadBitstreamA](#) and [ADMXRC3_LoadBitstreamW](#) return a failure status code.
2. Fixed a kernel-mode memory leak that can occur when a device handle is closed when at least one non-blocking operation has not been finished.
3. Fixed a bug in [ADMXRC3_SetClockFrequency](#) where on failure, the wrong status code was returned.

Release 1.1.0

Corrections:

1. Fixed a potential memory corruption issue on some architectures due to incorrect byte size calculation in device context structure.
2. Fixed a memory leak related to CFI Flash functionality when stopping and restarting the driver.
3. Fixed user VPD area (VPD space address range 0x100000-0x1FFFFFF) not being accessible on ADM-XRC-6TL and ADM-XRC-6T1.

- Corrected error codes returned by several ADMXRC3 API functions when invalid parameters are passed:
 - Non-Locked DMA functions now return 'ADMXRC3_INVALID_BUFFER' if the 'pBuffer' and/or 'length' parameters are invalid.
 - Locked DMA functions now return 'ADMXRC3_INVALID_BUFFER_HANDLE' if the 'hBuffer' parameter is invalid.
 - ADMXRC3_Unlock now returns 'ADMXRC3_INVALID_BUFFER_HANDLE' if the 'hBuffer' parameter is invalid.
- Fixed 'ADMXRC3_Unconfigure' failing with 'ADMXRC3_NOT_OWNER' even when the target FPGA has no owner.
- Fixed the ADMXRC3 DMA functions not properly co-validating the 'local' and 'length' parameters.
- Fixed the ADMXRC3 Locked DMA functions not properly co-validating the 'offset' and 'length' parameters.
- Fixed the ADMXRC3 nonblocking DMA functions incorrectly returning 'ADMXRC3_SUCCESS' instead of 'ADMXRC3_PENDING' when 'length' is zero and all other parameters are OK.
- Fixed the ADMXRC3 non-Locked DMA functions not properly co-validating the 'pBuffer' and 'length' parameters.
- Fixed certain ADMXRC3 API functions not trapping NULL pointers being passed, typically resulting in an application crash.
- Fixed a bug in driver parameter handling so that driver parameters are now correctly applied and have correct default values.
- Implemented VPD write-protection mechanism (now protected by default).
- Added workaround for 4k crossing issue in ADB3 Bridge rev 0x01 and earlier. Workaround is not applied for rev 0x02 or later.

Enhancements:

- Added support for ADM-XRC-6T1.
- Added API function ADMXRC3_OpenEx, which allows an unprivileged process to open a device in "read only" mode and use a subset of the ADMXRC3 API functions.
- Added API functions ADMXRC3_StartNotificationWait and ADMXRC3_FinishNotificationWait, which allow Linux applications to wait for events (since there is no Linux equivalent of ADMXRC3_RegisterWin32Event).
- Added API function ADMXRC3_GetCardInfoEx (with structure ADMXRC3_CARD_INFOEX), which returns a superset of data supplied by ADMXRC3_GetCardInfoEx.
- Added diagnostic API functions ADMXRC3_GetSensorInfo and ADMXRC3_ReadSensor, with associated types and structures. These functions allow applications to monitor the health of a Gen 3 reconfigurable computing card.

Release 1.0.0

This is the first release of the ADB3 Driver for Linux.