ALPHA DATA

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ADM-PCIE-6S1 SDK 5.0.1 for Linux Release Note

Introduction

This release note accompanies the ADM-PCIE-6S1 SDK for Linux. The latest version of this SDK can be found at:

ftp://ftp.alpha-data.com/pub/admxrc/linux

For support, send e-mail to support@alpha-data.com

Operating systems supported

This release of the ADM-PCIE-6S1 SDK supports the following operating systems:

- GNU/Linux distribution with 2.6.x kernel or 3.x kernel

Hardware supported

This release of the ADM-PCIE-6S1 SDK for Linux supports the following Alpha Data models:

- ADM-PCIE-6S1

Related products

ADM-XRC Driver for Linux, release 4.0.3 or later, is required to run the examples in this SDK. We recommend using the latest version of the driver that is available from:

ftp://ftp.alpha-data.com/pub/admxrc/linux

Installation instructions

This release of the SDK is distributed as a .tar.gz file. To install it, unzip it to a location in the filesystem of your choice.

After installation, and before running any example applications, set the environment variable **ADMXRC_SDK5** to point to the root of the SDK. For example:

```
export ADMXRC_SDK5=/opt/my_testing/admpcie6s1_sdk-5.0.1
```

License Agreement

After installation, the license agreement for this software is available in the folder where the software is installed, in the file **license.txt**. Please contact Alpha Data if alternative licensing conditions are required.

Alpha Data reserves the right to use a different license agreement for future releases of this software.

Release history

Release 5.0.1

> **Note: Recommended Xilinx ISE version**
> Xilinx ISE 14.7 was used to generate the pre-built bitstreams (.bit files) in this release of the SDK, and as such, is recommended. Although it is not anticipated that other versions of Xilinx ISE will produce defective .bit files, Alpha Data cannot guarantee that another version of the Xilinx ISE tools will produce the same quality of results.

Corrections:

1 Example applications:

(a) Added a 10 millisecond delay after configuring the FPGA (e.g. by a call to **ADMXRC2_ConfigureFromFile**) in order to give any DCMs distributing the local bus clock (**LCLK**) time to achieve lock before any attempt by software to access registers in the FPGA. This resolves a problem seen with the **itest** example application where no interrupts are generated.

Behavior changes:

2 Example applications:

(a) The **master** example application now configures the Direct Master local bus to PCI window to be from 0xFF000000 to 0xFF7FFFFF (previously 0xFFC00000 to 0xFFFFFFFF).

3 Example FPGA designs:

(a) The **master** example FPGA design now generates local bus cycles (as local bus master) that target the new Direct Master local bus to PCI window from 0xFF000000 to 0xFF7FFFFF (previously 0xFFC00000 to 0xFFFFFFFF).

Release 5.0.0

> **Note: Recommended Xilinx ISE version**
> Xilinx ISE 14.5 was used to generate the pre-built bitstreams (.bit files) in this release of the SDK, and as such, is recommended. Although it is not anticipated that other versions of Xilinx ISE will produce defective .bit files, Alpha Data cannot guarantee that an arbitrary version of the Xilinx ISE tools will produce the same quality of results.

This is the first release of the ADM-PCIE-6S1 SDK for Linux.